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PATENT NUMBER and ISSUE DATE

	U.S. UIILITY F	atent A	pplication			}	
APPL NUM	FILING DATE	CLASS		GAU	EXAM		
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	d of fabricating an in	ntegrated	circuit that se	als a MEMS d	DEPT. OF COMM.P	a Cavity AT.8 TM-PTO-436L(
		Assistant Examiner		Total Claims	Prtn O.G	Print Claim fo	
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unt Due	Date Paid	1			Sheets Drwg.	Figs.Drwg.	Print F
			Prima	ry Examiner			
TERMINAL		PREPARED FOR ISSUE			Application Examiner		
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